

08/691822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



IN RE PATENT APPLICATION of

LEE

Continuation of

Application No.: 08/596,423

Group Art Unit: 2504

Examiner: T. Cunningham

Filed: Herewith

Title: SUBSTRATE BIAS GENERATOR IN
SEMICONDUCTOR MEMORY DEVICE

* * * * *

August 5, 1996

INFORMATION DISCLOSURE STATEMENT

Attached is Form PTO-1449 listing the enclosed documents.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Respectfully submitted

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By

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